

Accelerating Time-To-Science in Geophysical Simulations Ignacio Sarasua & Filippo Spiga isarasua@nvidia.com fspiga@nvidia.com





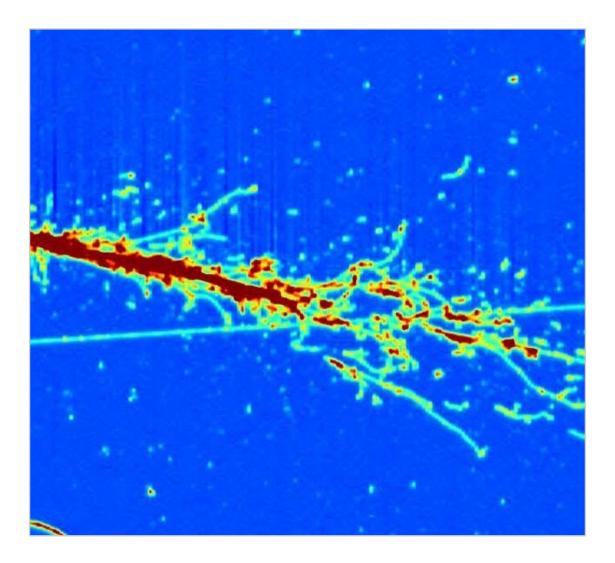
THIS INFORMATION IS INTENDED TO OUTLINE OUR GENERAL PRODUCT DIRECTION. MANY OF THE PRODUCTS AND FEATURES DESCRIBED HEREIN REMAIN IN VARIOUS STAGES AND WILL BE OFFERED ON A WHEN-AND-IF-AVAILABLE BASIS. THIS ROADMAP DOES NOT CONSTITUTE A COMMITMENT, PROMISE, OR LEGAL OBLIGATION AND IS SUBJECT TO CHANGE AT THE SOLE DISCRETION OF NVIDIA. THE DEVELOPMENT, RELEASE, AND TIMING OF ANY FEATURES OR FUNCTIONALITIES DESCRIBED FOR OUR PRODUCTS REMAINS AT THE SOLE DISCRETION OF NVIDIA. NVIDIA WILL HAVE NO LIABILITY FOR FAILURE TO DELIVER OR DELAY IN THE DELIVERY OF ANY OF THE PRODUCTS, FEATURES, OR FUNCTIONS SET FORTH IN THIS DOCUMENT.

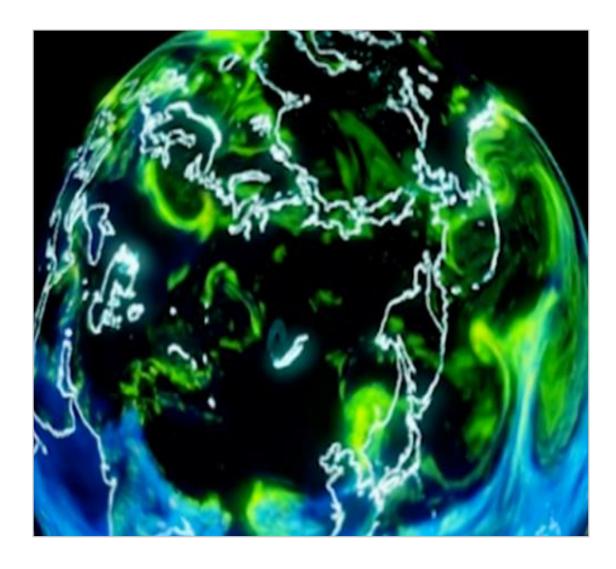


📀 NVIDIA

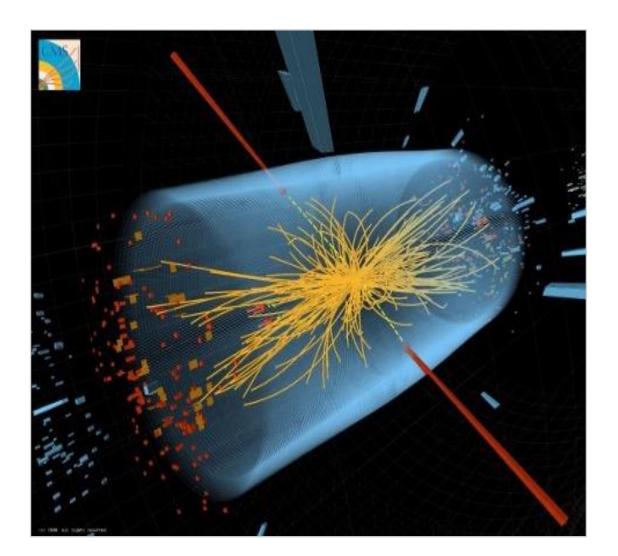
HPC Enabling Scientific and Industrial Achievements

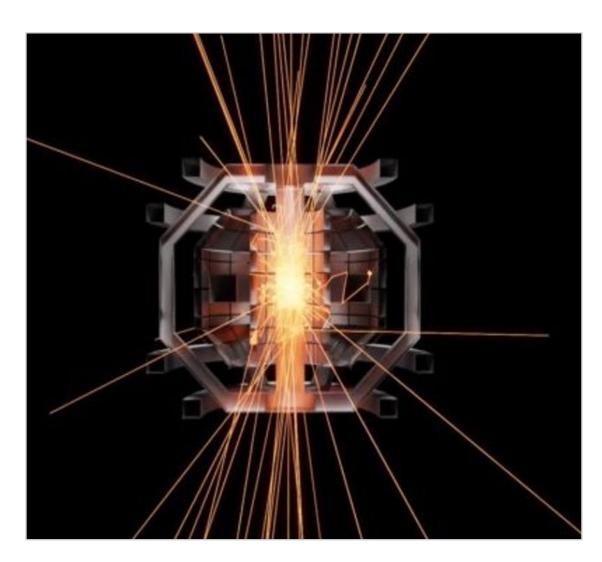
PARTICLE PHYSICS New Fundamental Particles





HIGH ENERGY PHYSICS **Collision Reconstruction**





WEATHER & CLIMATE FourCastNet

GENOMICS Record DNA Sequencing



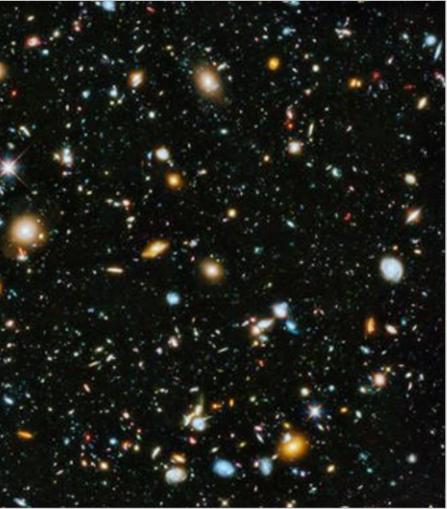


ENERGY Plasma Modeling with FNO

VIRUS PATHOLOGY GB Finalist: CoVID variant classifier



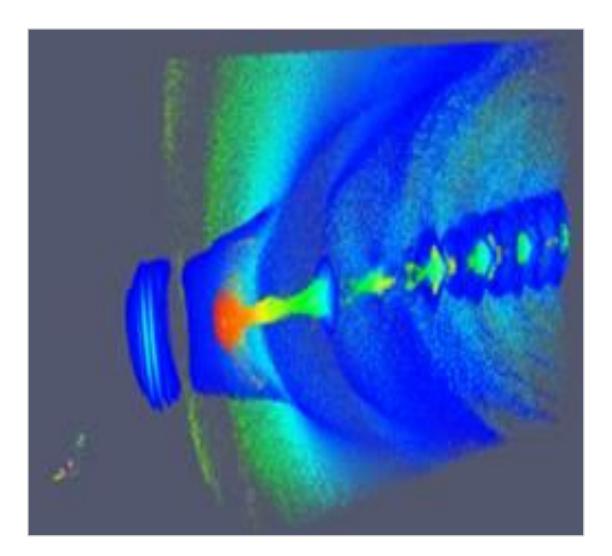
COSMOLOGY ID Dark Matter



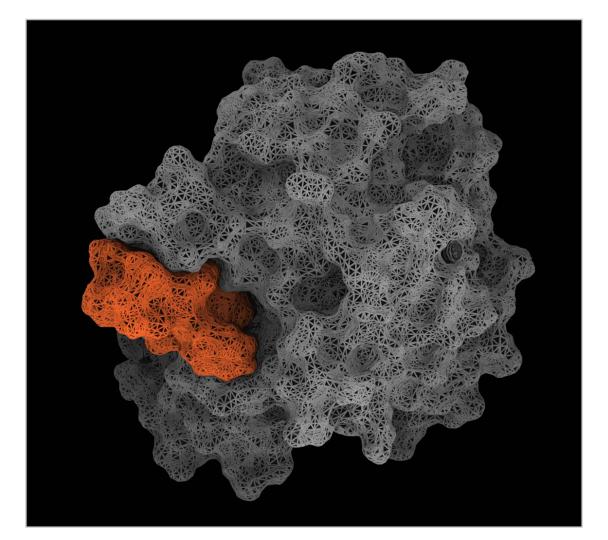
ASTROPHYSICS DL Classifies Distant Galaxies



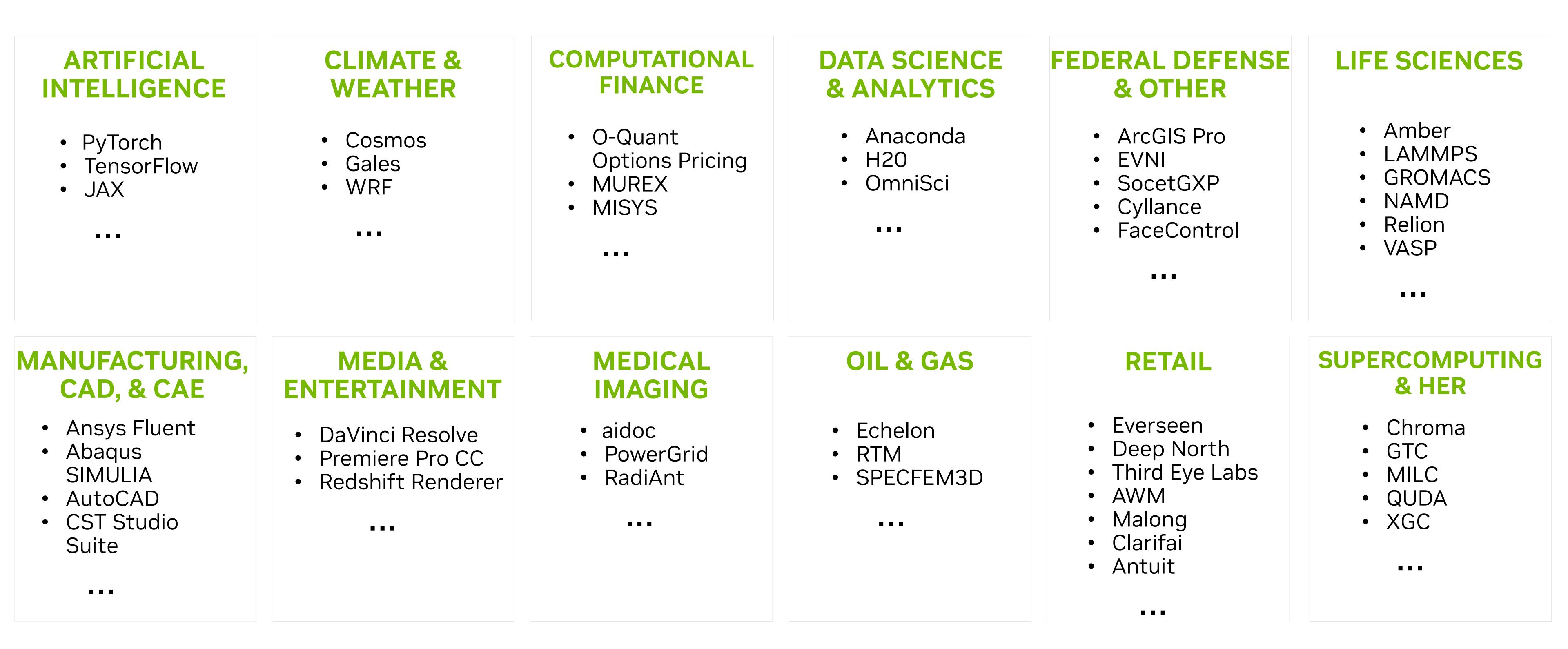
RADIOLOGY **GB:** Laser Electron Accelerators



LIFE SCIENCES GB: Protein Similarity Search





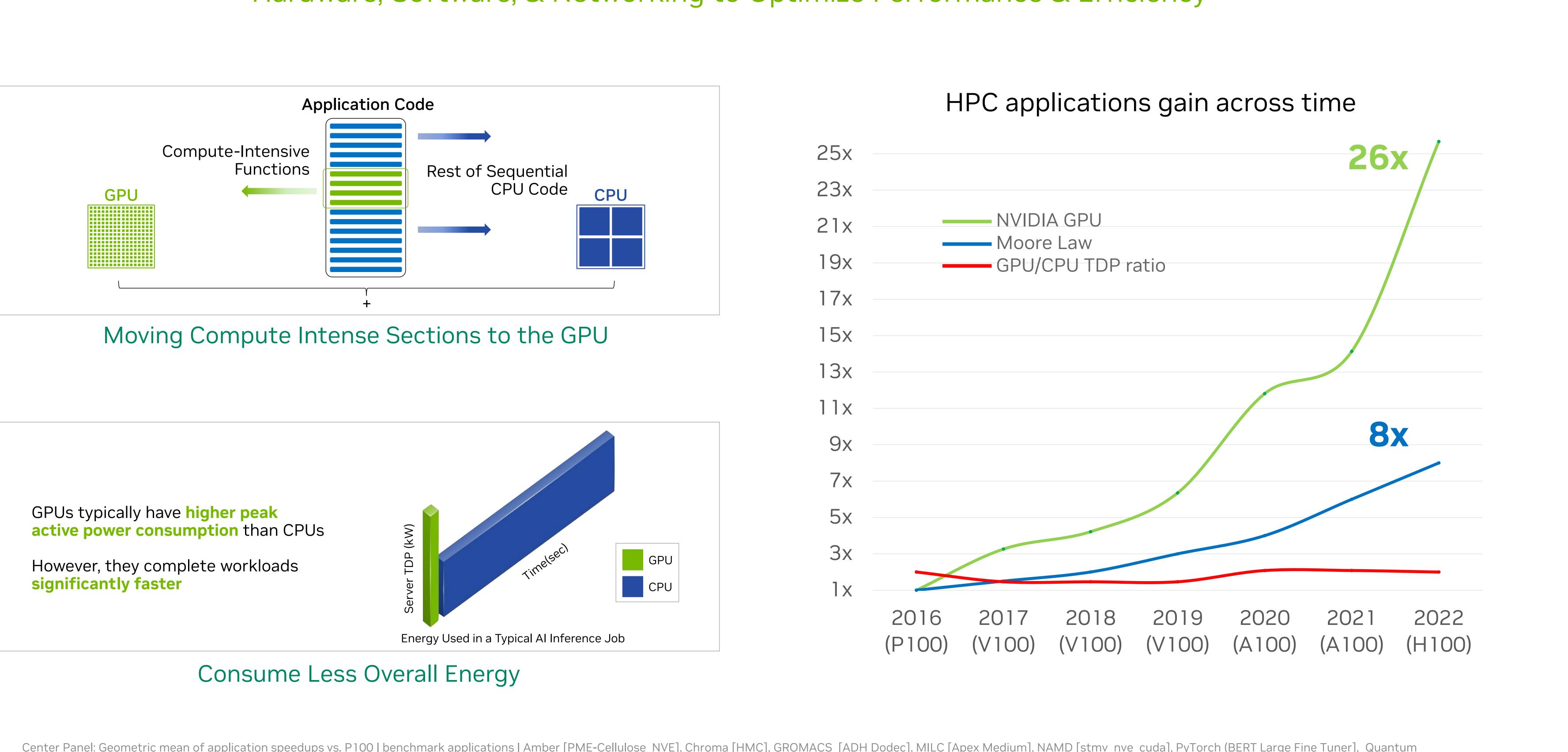


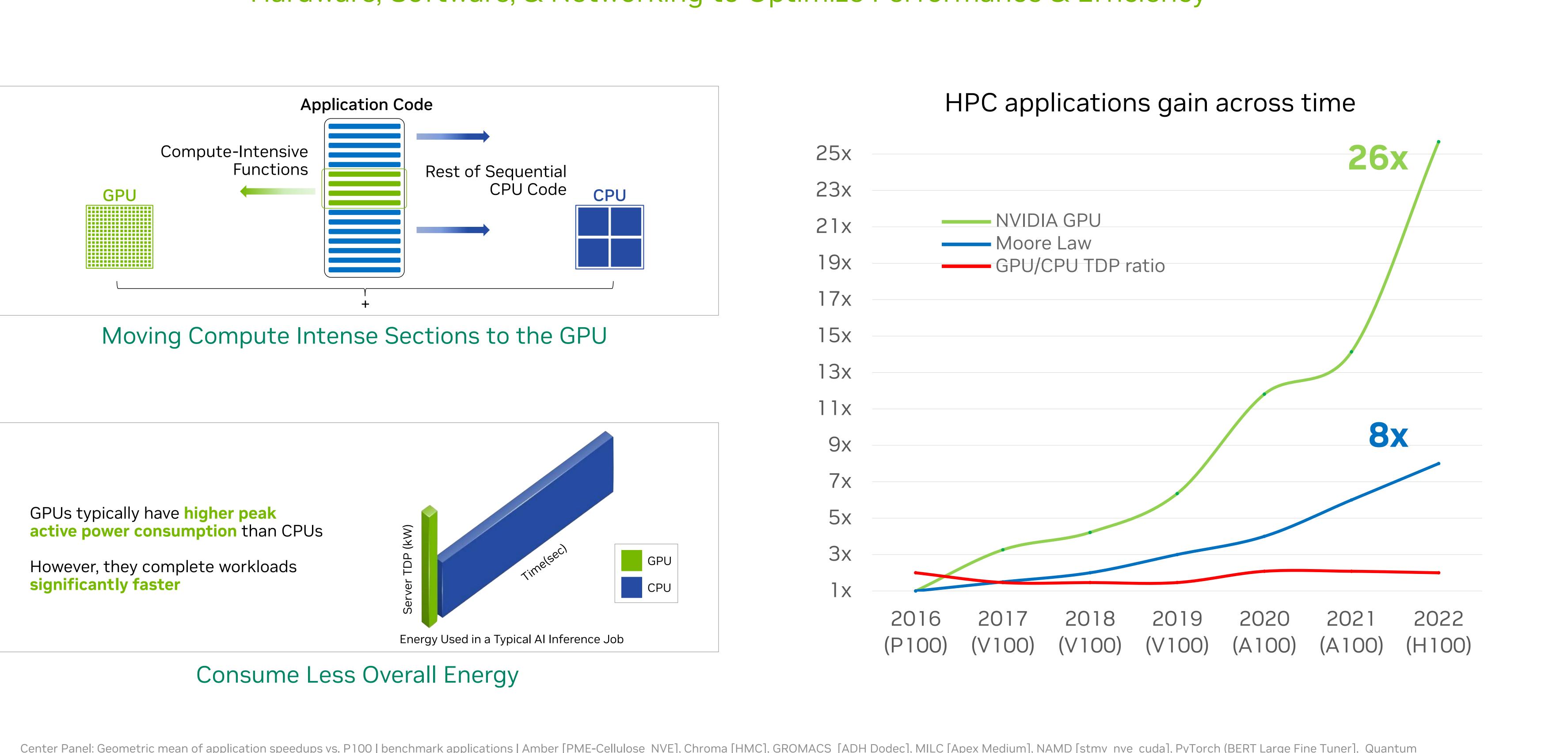
For a comprehensive list of all apps, please refer to GPU application catalog: https://www.nvidia.com/content/dam/en-zz/Solutions/Data-Center/tesla-product-literature/gpu-applications-catalog.pdf

2700+ GPU-Accelerated Applications Plus many accelerated frameworks!









Center Panel: Geometric mean of application speedups vs. P100 | benchmark applications | Amber [PME-Cellulose_NVE], Chroma [HMC], GROMACS [ADH Dodec], MILC [Apex Medium], NAMD [stmv_nve_cuda], PyTorch (BERT Large Fine Tuner], Quantum Espresso [AUSURF112-jR]; TensorFlow [ResNet-50], VASP 6 [Si Huge], [GPU node: with dual-socket CPUs with 4x P100, V100, or A100 GPUs. H100 values shown for 2022 projected performance subject to change

Why Accelerated Computing is Energy Efficient Hardware, Software, & Networking to Optimize Performance & Efficiency

NVIDIA H100

Unprecedented Performance, Scalability, and Security for Every Data Center

Highest AI and HPC Performance

4PF FP8 (6X) 2PF FP16 (3X) 1PF TF32 (3X) 67TF FP64 (3.4X) 3.35TB/s (1.5X), 80GB HBM3 memory

Transformer Model Optimizations

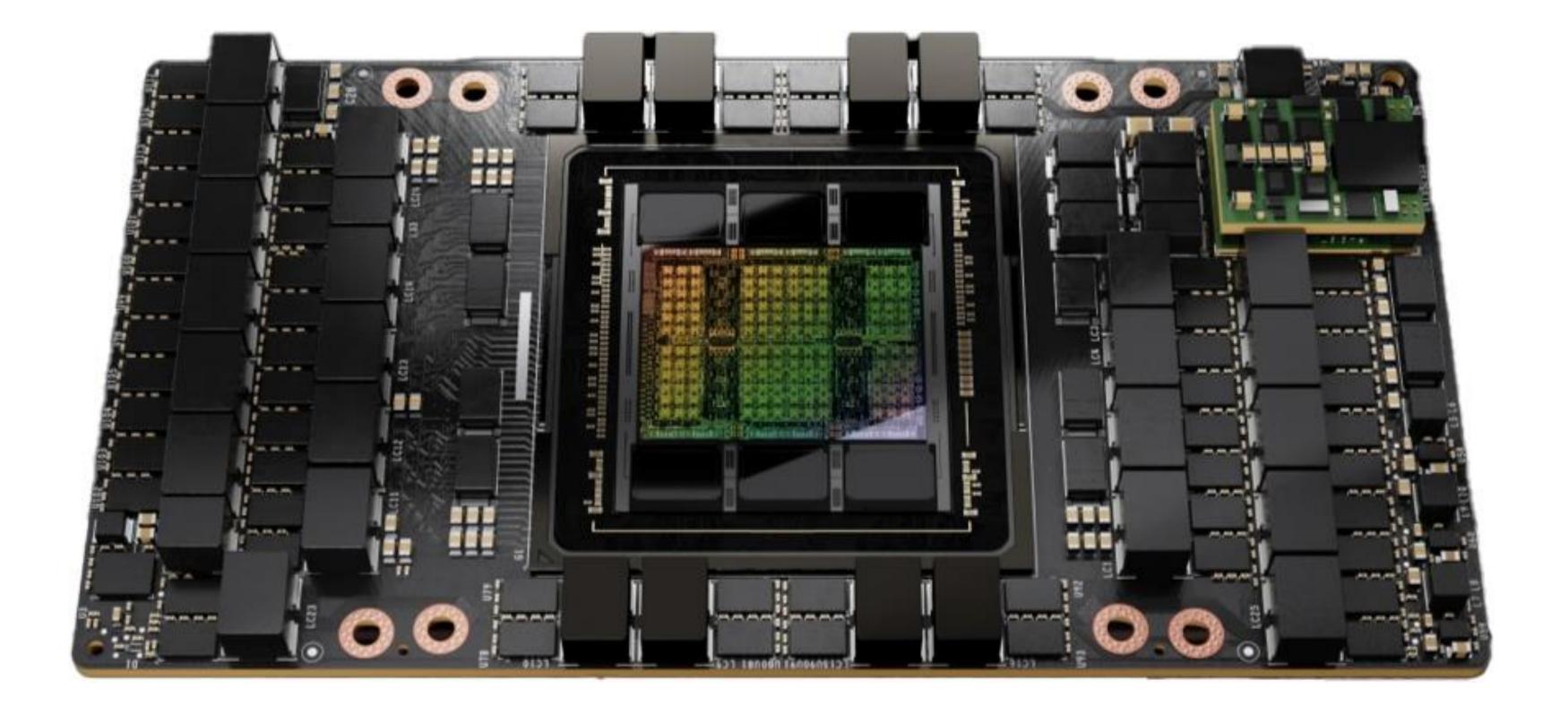
6X faster on largest transformer models

Highest Utilization Efficiency and Security

7 Fully isolated & secured instances, guaranteed QoS 2nd Gen MIG | Confidential Computing

Fastest, Scalable Interconnect

900 GB/s GPU-2-GPU connectivity (1.5X) up to 256 GPUs with NVLink Switch | 128GB/s PCI Gen5



FP8, FP16, TF32 performance include sparsity. X-factor compared to A100

ACCELERATED STANDARD LANGUAGES

ISO C++, ISO Fortran

```
std::transform(par, x, x+n, y, y,
    [=](float x, float y) { return y +
a*x; }
);
```

```
do concurrent (i = 1:n)
  y(i) = y(i) + a*x(i)
enddo
```

import cunumeric as np

```
def saxpy(a, x, y):
   y[:] += a*x
```

 $\bullet \bullet \bullet$

Core

Math

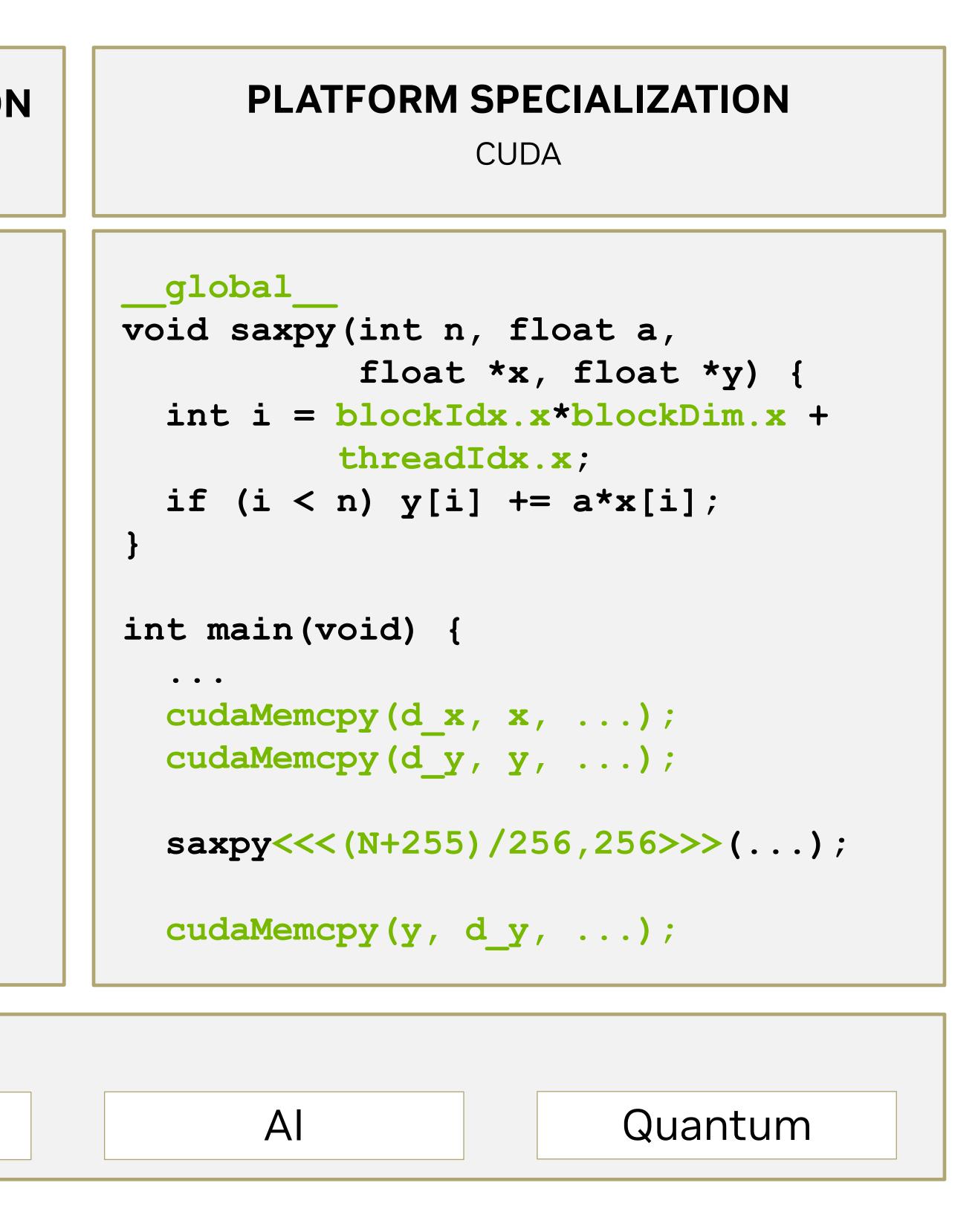
Programming the NVIDIA platform CPU, GPU, and Network

5	INCREMENTAL PORTABLE OPTIMIZATIO OpenACC, OpenMP
	<pre>#pragma acc data copy(x,y) { std::transform(par, x, x+n, y, y, [=](float x, float y) { return y + a*x; }); }</pre>
	<pre>#pragma omp target data map(x,y) { std::transform(par, x, x+n, y, y, [=](float x, float y){ return y + a*x; }); }</pre>

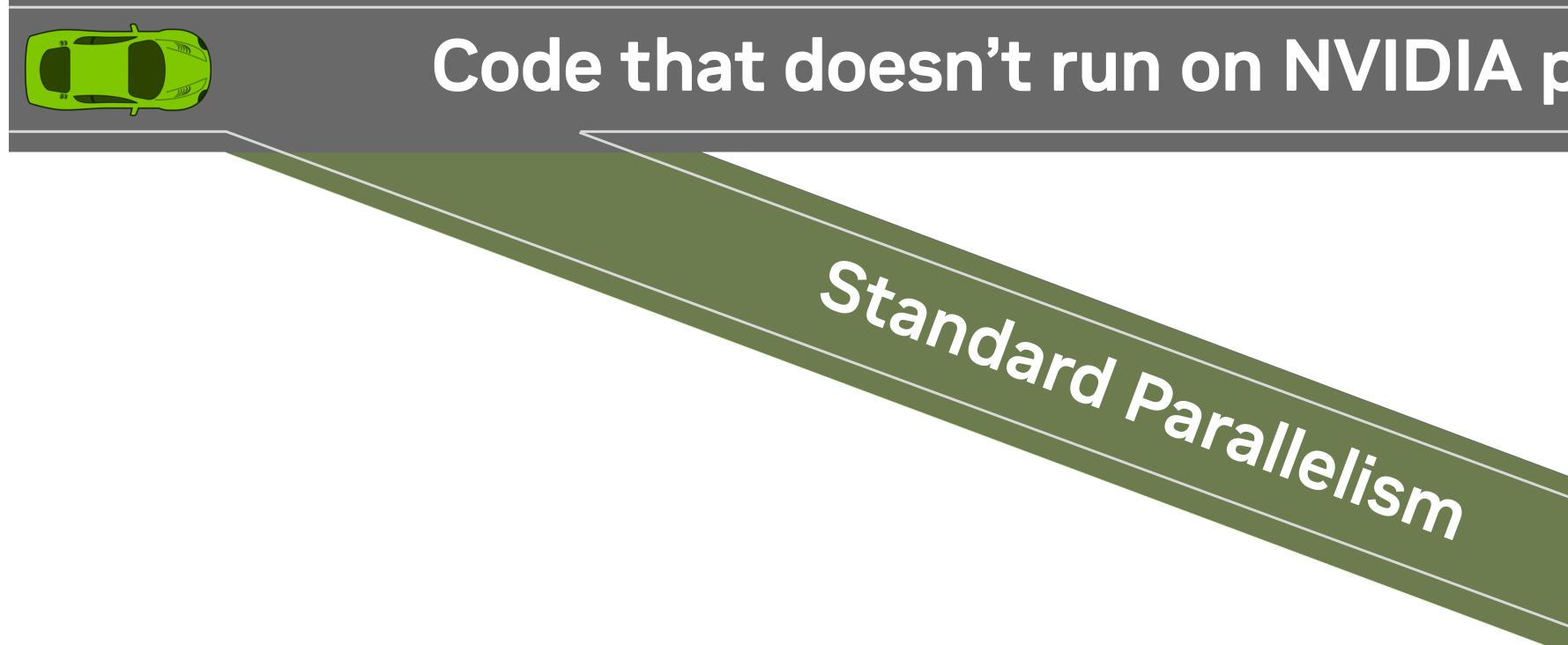
ACCELERATION LIBRARIES

Communication

Data Analytics



9



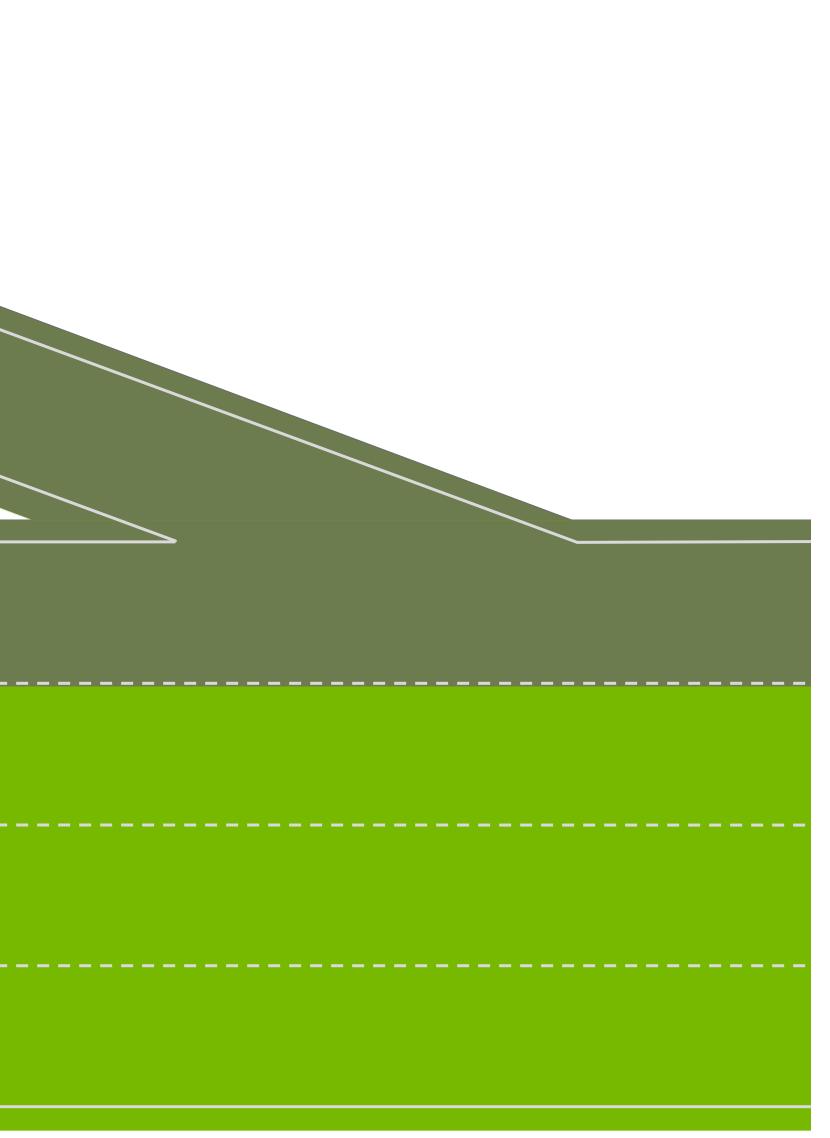
Scientists Need On-Ramps Promote Parallelism, not Heterogeneity

Code that doesn't run on NVIDIA platforms

10x Perf vs Host-Only Lane

CUDA C++ & Fortran **GPU Speed of Light Lane**

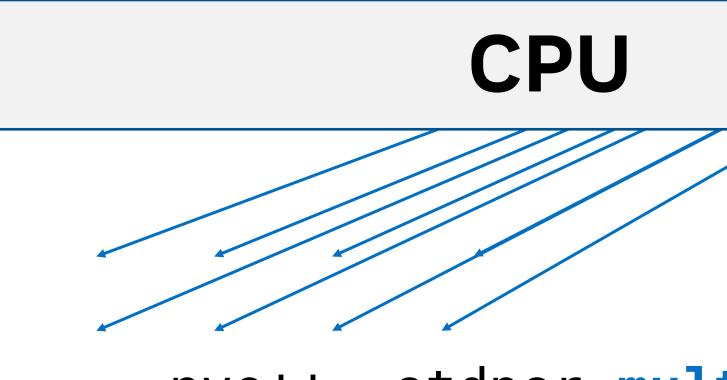






ISO C++

std::transform(par, x, x+n, y, y,[=](float x, float y){ return y + a*x;



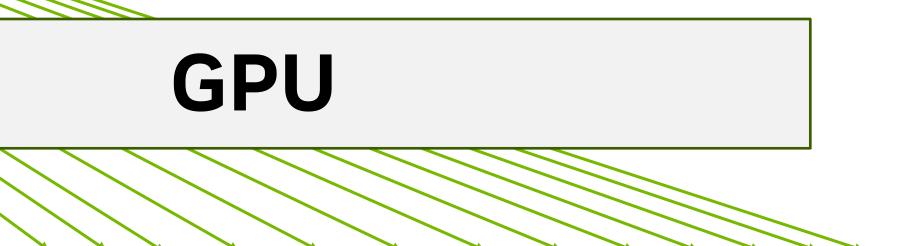
nvc++ -stdpar=**multicore** nvfortran -stdpar=multicore legate -cpus 16 saxpy.py

Accelerated Standard Languages Parallel performance for wherever your code runs

ISO Fortran

do concurrent (i = 1:n) y(i) = y(i) + a*x(i)enddo

> nvc++ -stdpar=gpu nvfortran -stdpar=gpu legate -gpus 1 saxpy.py

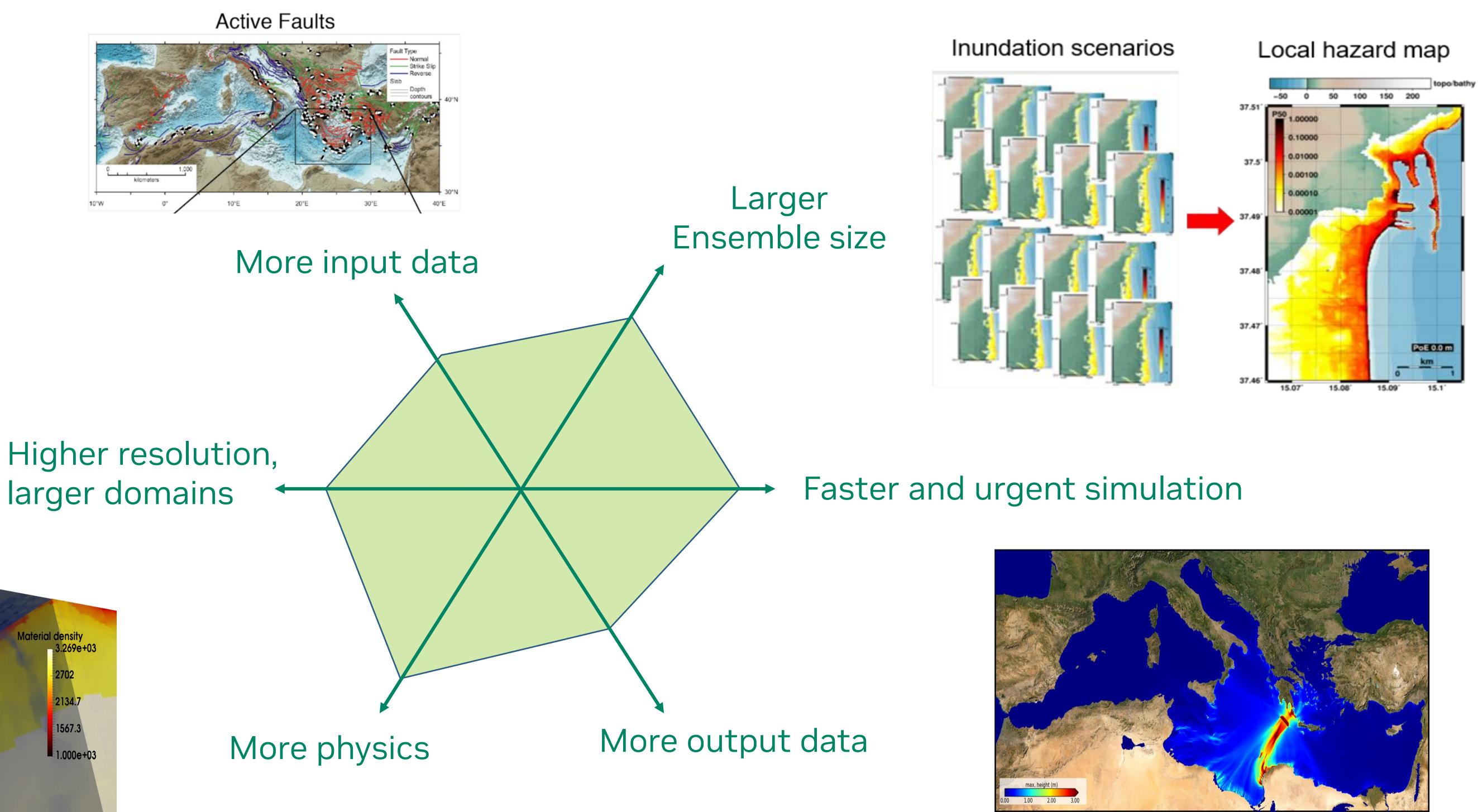


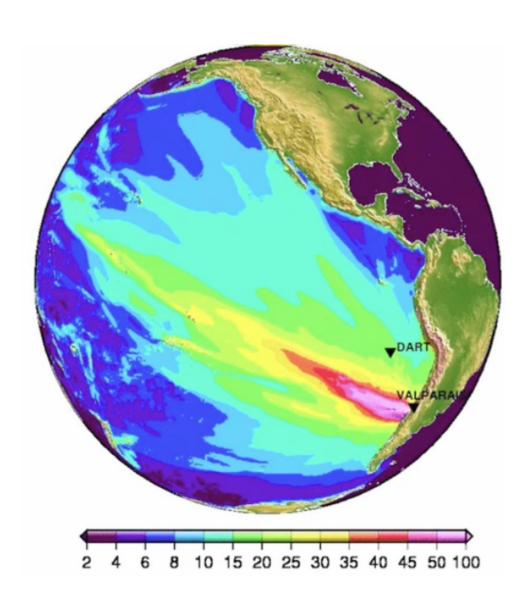
import cunumeric as np def saxpy(a, x, y): y[:] += a*x

Python

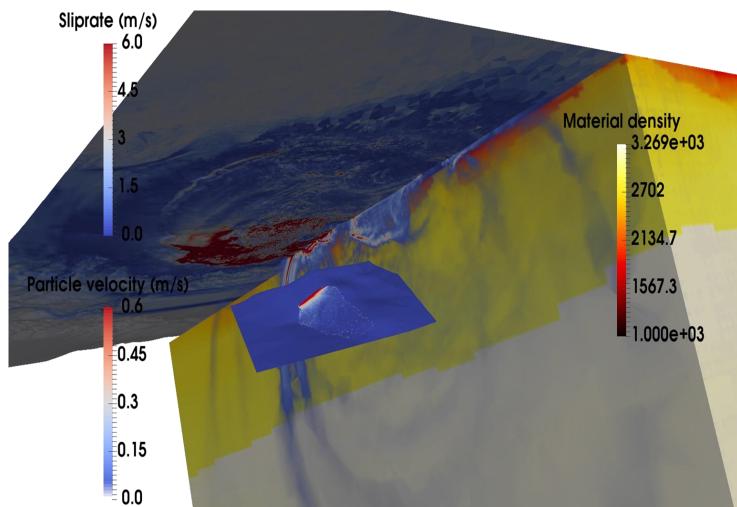
	_	_	_	_	_	_	_	_	_	_	_	_	_	-	-	_	_	_	_	
																				5
																				÷.
																				÷.
																				i.
																				i.
																				1
																				÷.
																				4
																				А.
																				÷.
																				÷.
																				i.
																				÷.
																				÷.
																				÷.
																				÷.
-	-	_	_	_	_	_	_	_	_	_	_	_	_	_	_	_	1			i.
																	L			I.
																	L			1
																	L			4
																	L			÷
																	L			÷.
																	L			i.
																	L			i.
																	L			
																	L			5
																	L			А.
																	L			й.
																	L			i.
																	L			i.
																	L			
																	L			5
																	L			÷
																	L			÷.
																	L			i.
																	L			i.
																	L			I.
																	L			4
																	L			4
																	L			й.
																	L			i.
																	L			i.
																	L			
																	L			4
																	L			
																	L			ĵ.
																				Į.
																	L			ļ.
																	L			i.
																	L			Î.
																				I.
																	L			ļ.
																				÷.
																	L			÷.
																	L			ĵ.
																				Ĩ
														-						
																				ł
																				i.
																				î.
																				Ĩ.
1		-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	1

GPU Acceleration Enables Breakthrough New Science From **Petascale** (~10⁴-10⁵ CPU) to **Exascale** (×1000!)





larger domains



Data and visual assets courtesy of Stefano Lorito (INVG) and Manuela Volpe (INGV). Work supported by EU ChEESE Centre of Excellence in HPC Applications.



Application	Programming Language	GPU	MPI+X			
SeisSol	C/C++	Yes	CUDA			
SPECFEM3D	Fortran	Yes	CUDA			
ExaHyPE	C/C++	Partly	CUDA			
xSHELLS	C/C++	Partly	CUDA			
HySEA	C/C++	Yes	CUDA			
FALL3D	Fortran	Yes	OpenACC			

GPU-accelerated ChEESE applications

https://cheese-coe.eu/

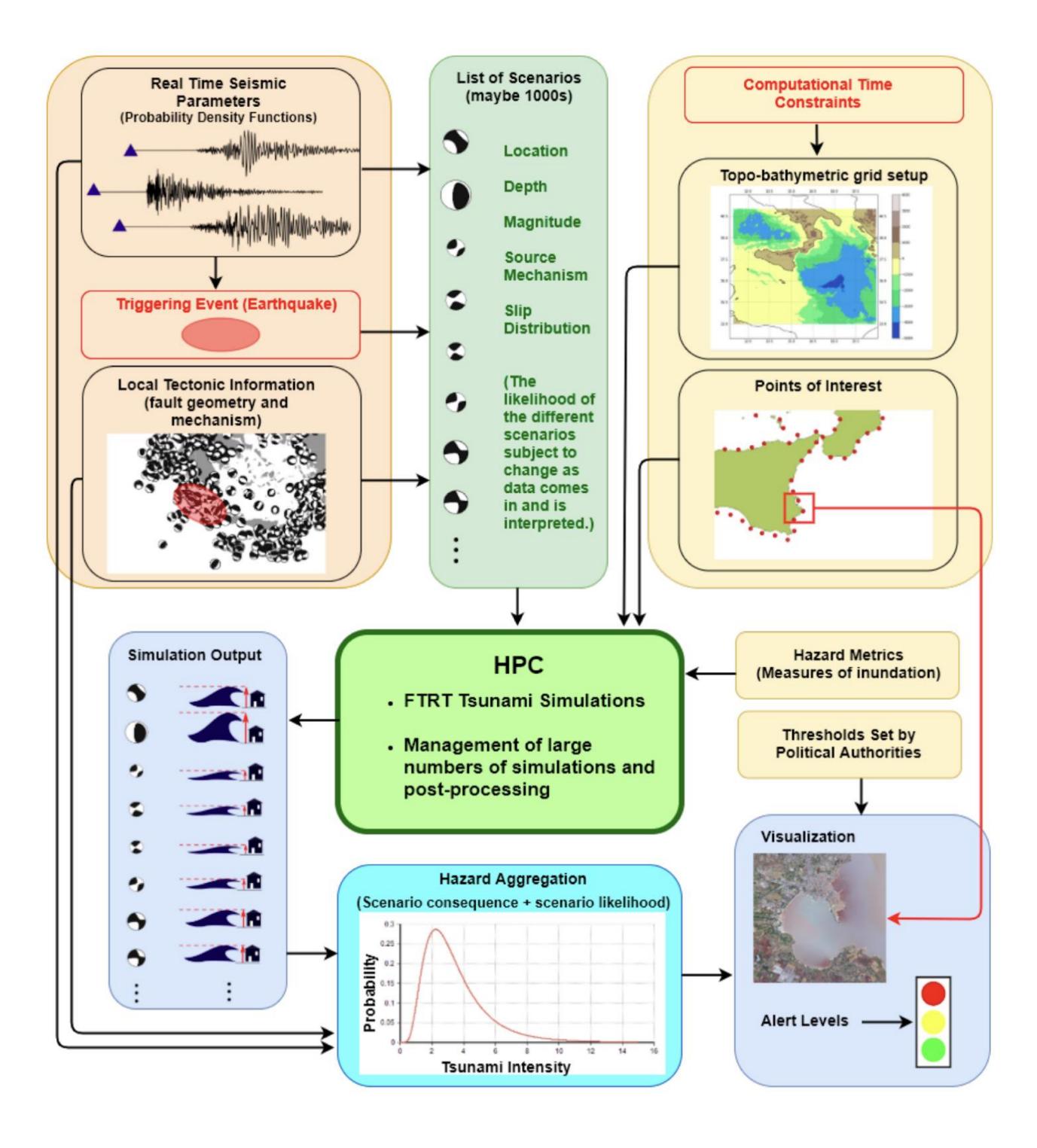
Data and visual assets courtesy of Piero Lanucara (CINECA) and Giorgio Amati (CINECA). Work supported by EU ChEESE Centre of Excellence in HPC Applications.

ChEESE

Center of Excellence for Exascale in Solid Earth



GPU-enabled ChEESE workflow The ChEESE PD8: Probabilistic Tsunami Forecasting (PTF) for early warning and rapid post event assessment



Urgent computing workflow exercise on Marconi-100

Code: Tsunami-HySEA (TRL 6~7)

<u>Scenario</u>: On-the-fly simulations for POST-EVENT ASSESSMENT (tens of minutes) - NO Early Warning - the Samos Earthquake

- \bullet

Outputs: ~38 000 scenario simulations

<u>Resources</u>: 805 nodes (3,220 V100 GPUs), ~25.7 PFlop/s

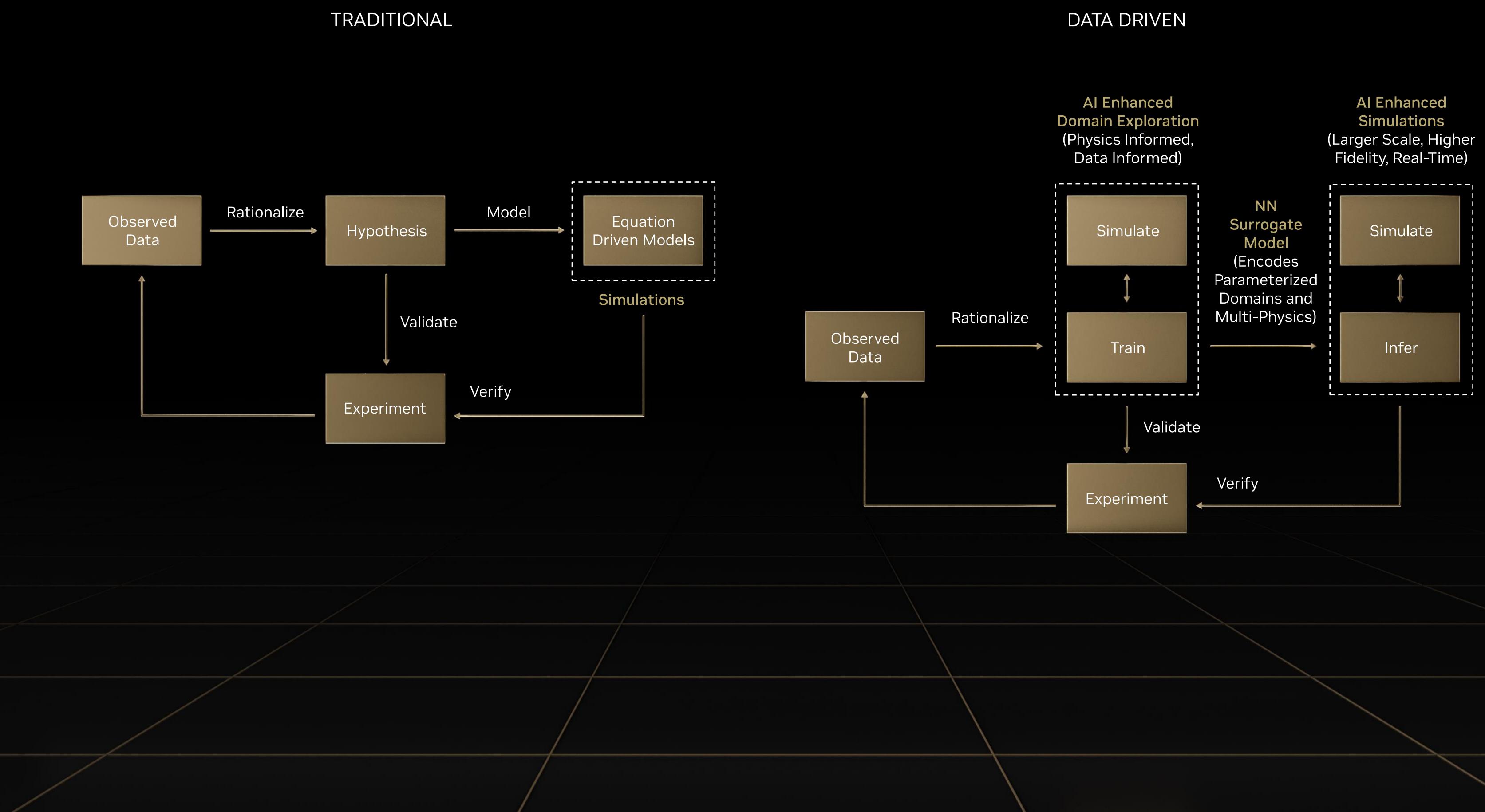
 Simulation ensembles to be run from scratch on large enough HPC clusters in urgent computing mode. Provides exceedance probabilities for tsunami heights just off the coastline for almost equally spaced points of interest every 2 km in front of the coasts of the Mediterranean Sea



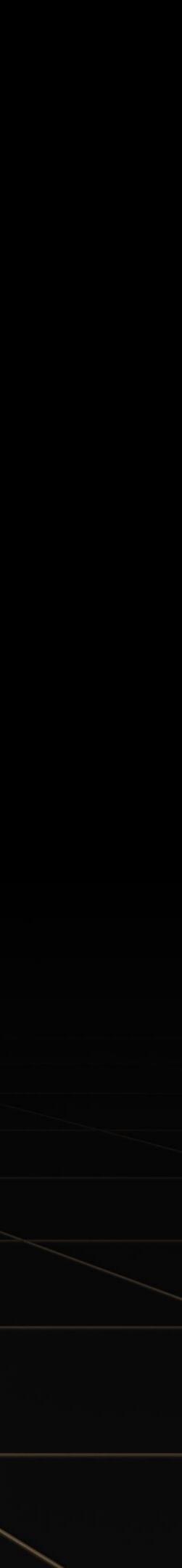


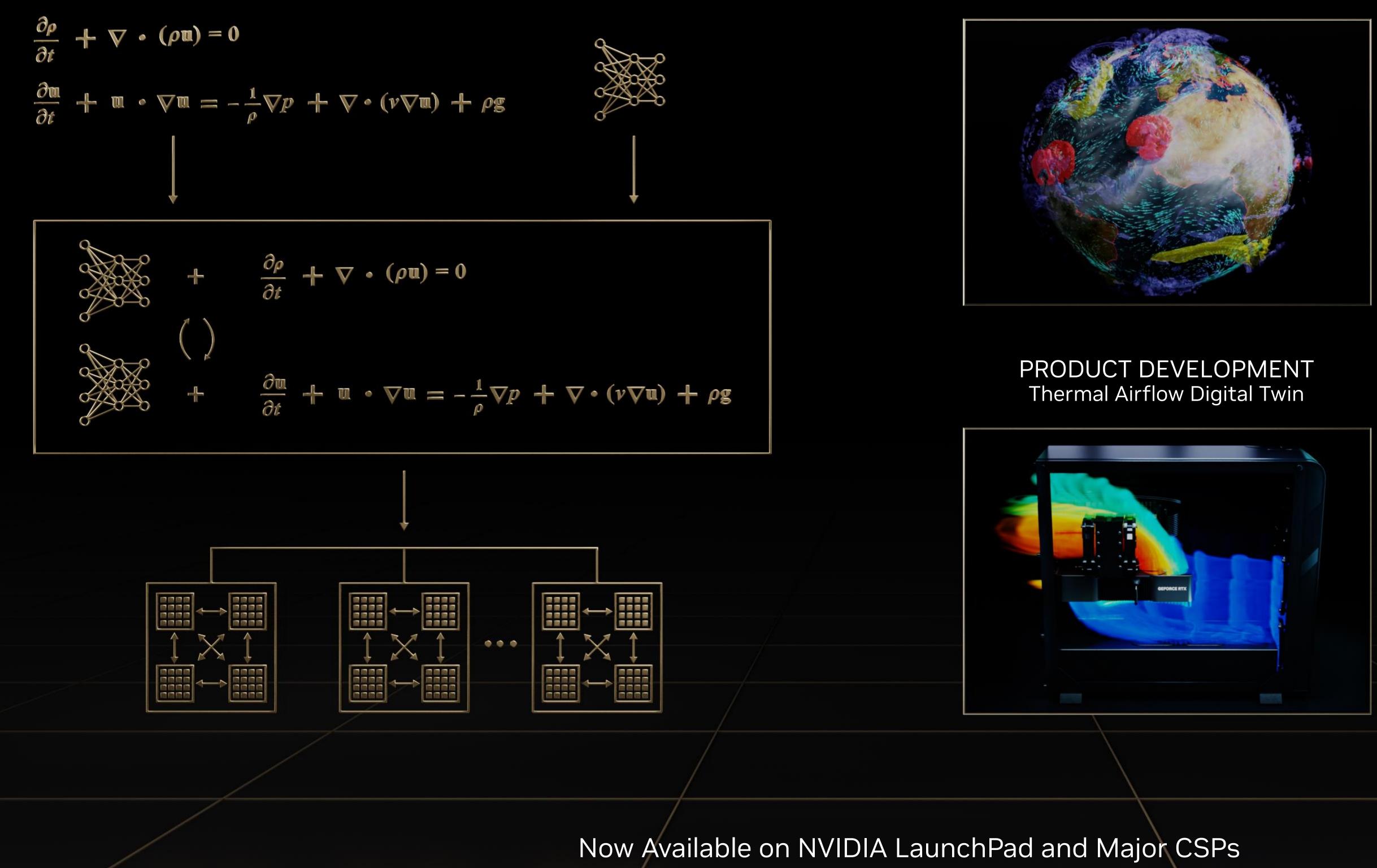
Beyond HPC HPC+Al & Digital Twins





AI IS THE 4TH PILLAR OF SCIENTIFIC DISCOVERY





NVIDIA MODULUS

Shortcut to Surrogate Models for Interactive Simulation | Development Platform for Al Surrogate Models

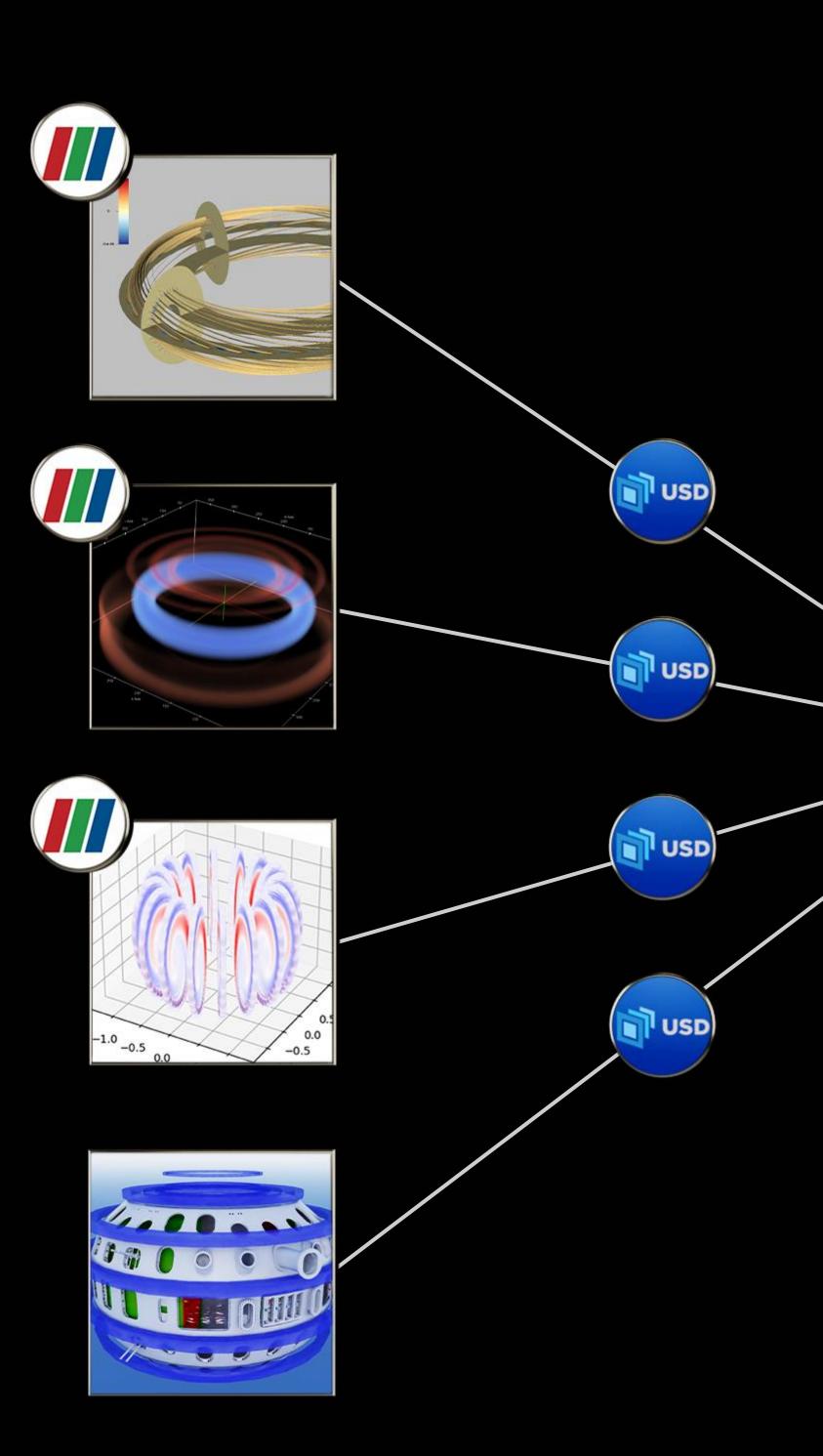
WEATHER & CLIMATE MODELING FourCastNet

RENEWABLE ENERGY Siemens Gamesa Wind Farm



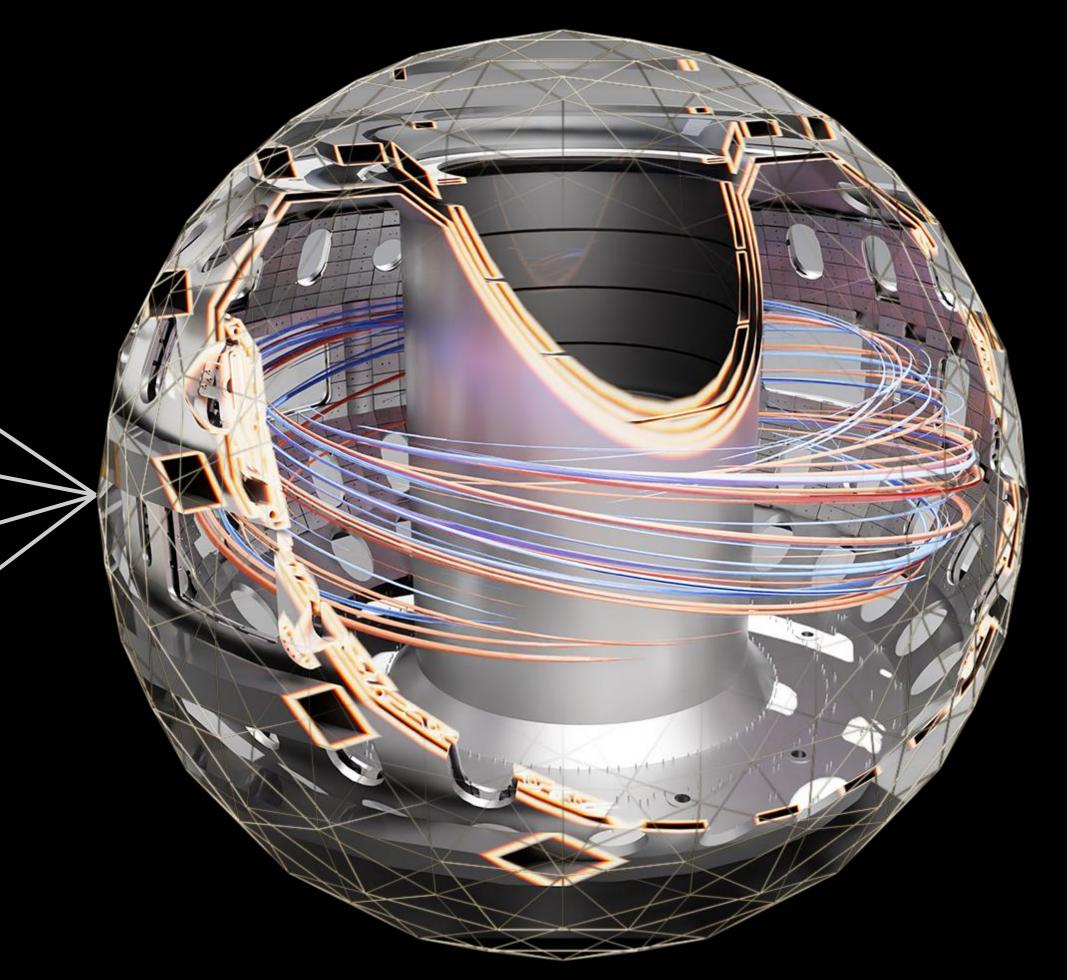
INDUSTRIAL DIGITAL TWIN Data Center CFD Acceleration





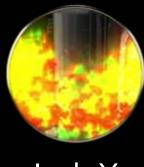
NVIDIA OMNIVERSE FOR SCIENTIFIC COMPUTING Connecting Complex HPC 3D and Simulation Workflows

Omniverse Nucleus Central Database





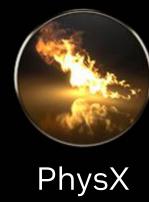
RTX Rendering



IndeX Rendering



NeuralVDB





AI & Modulus



Portal



ABOUT NVAITC

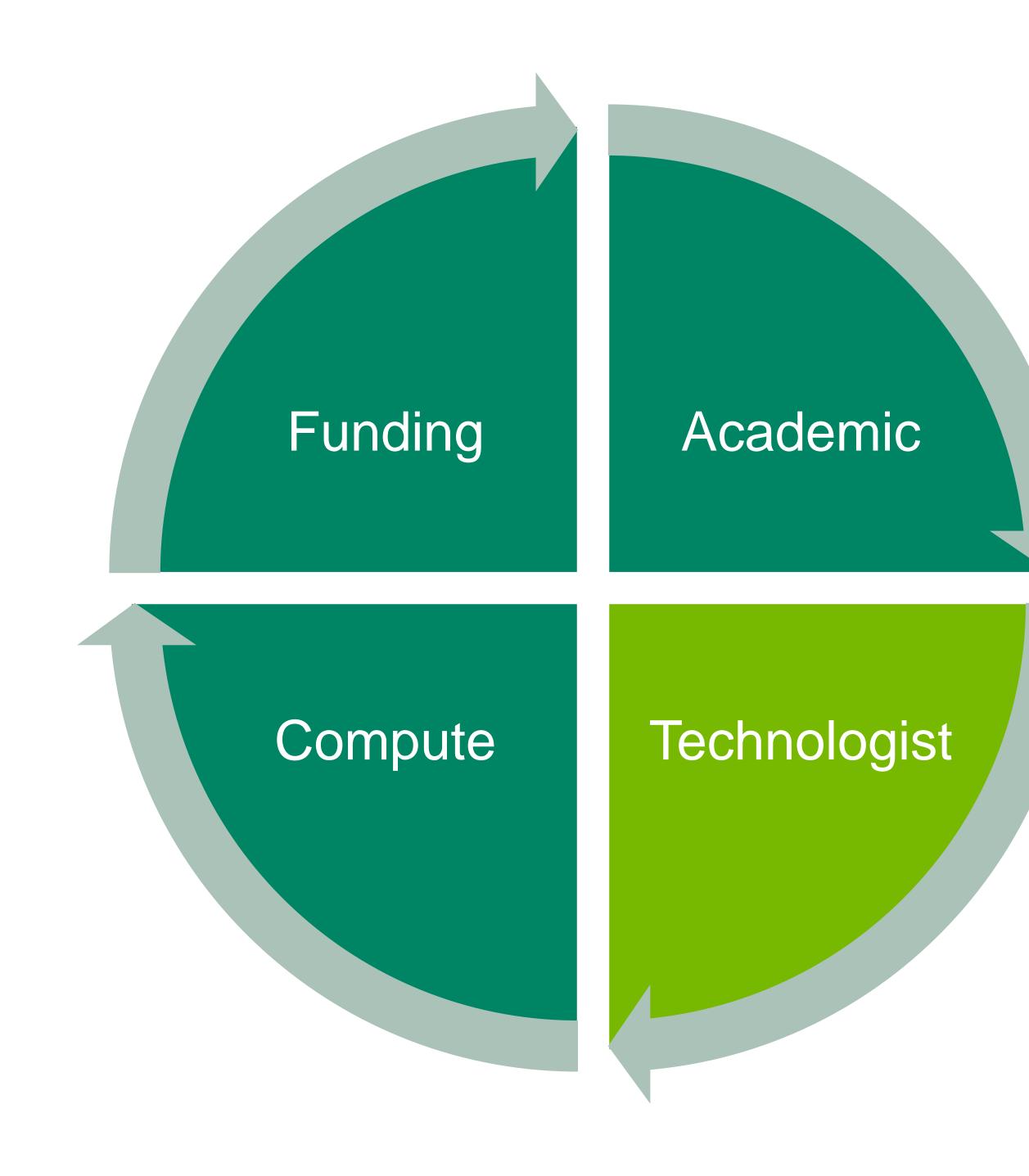




Industry Collaboration

NVIDIA CONFIDENTIAL. DO NOT DISTRIBUTE.

RESEARCH COLLABORATIONS Enabling AI research through efficient GPU Computing



Scientific Collaboration



workstation



Each research has its own GPU for developing/debugging purposes.

They all use the same software stack and push developed code to a private *github* organization.



Local or Remote development

local cluster



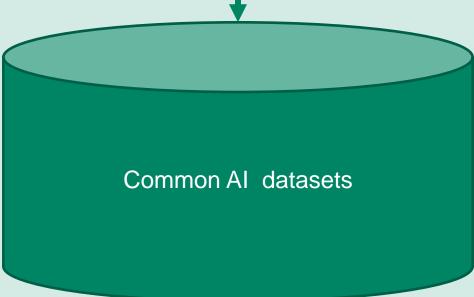
~48 GPUs

Researchers access to resources through SLURM. According to priorities, seniority, SLURM partitions access policy may vary. Senior researchers operate the local cluster. Containers are pulled from NVIDIA NGC.

national facility







~5000 GPUs

Support for singularity, enroot. DATASETS_AI is a fileset containing the most common datasets (imagenet, epickitchens, meccano, synlidar, etc). Local conda channel (cineca-ai) to provide packages optimized for the local architecture. Containers are pulled from NVIDIA NGC.

are punca normanizia noc.





